

Notice of References Cited

Application/Control No.

09/873,580

Applicant(s)/Patent Under

Reexamination

FUJITA, HARUMITSU

Examiner

Fernando Toledo

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2823

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U.S. PATENT DOCUMENTS

SECRETARY DOCUMENTS							
★		Document Number		Date	Name	Classification	
		Country Code-Number-Kind Code				MM-YYYY	
	A	US-6015736-A		01-2000	Luning et al.	438	264
	B	US-6294430-B1		09-2001	Fatow et al.	438	264
	C	US-					
	D	US-					
	E	US-					
	F	US-					
	G	US-					
	H	US-					
	I	US-					
	J	US-					
	K	US-					
	L	US-					
	M	US-					

FOREIGN PATENT DOCUMENTS

PATENT DOCUMENTS							
★		Document Number	Date	Country	Name	Classification	
		Country Code-Number-Kind Code	MM-YYYY				
	N						
	O						
	P						
	Q						
	R						
	S						
	T						

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)					
	U	Wolf, Silicon Processing for the VLSI Era Volume 2: Process Integration; pp. 354 - 361; Lattice Press 1990					
	V						
	W						
	X						

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.